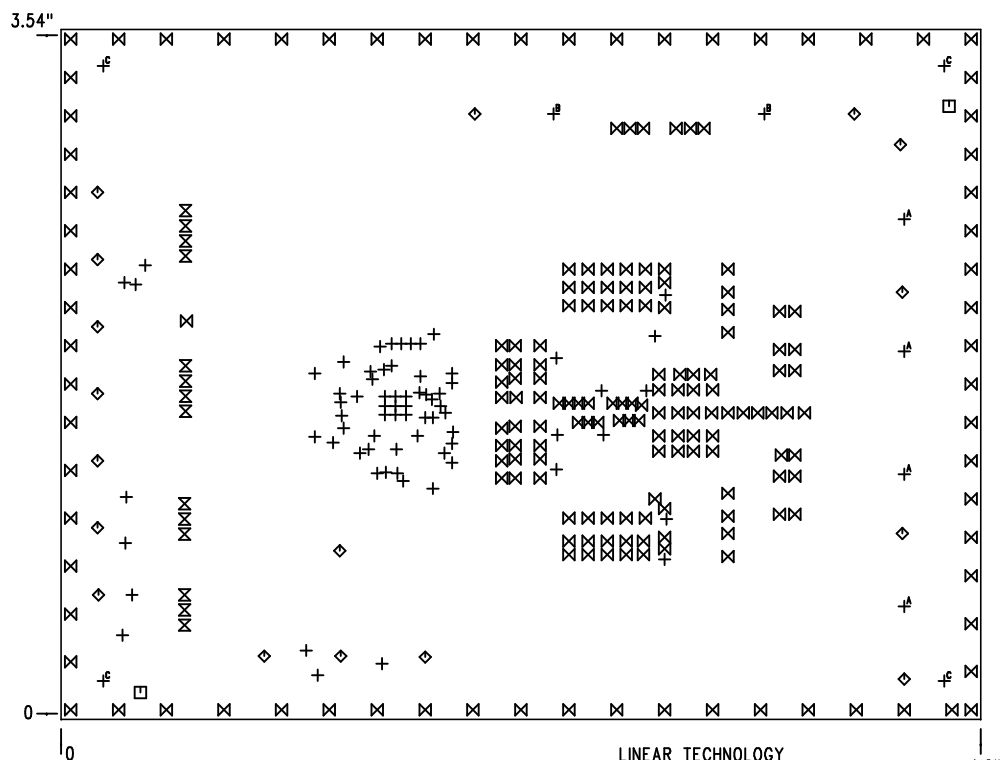


# REVISION HISTORY

ECO	REV	DESCRIPTION	APPR	DATE
1	3	PRODUCTION	GORAN P.	10-05-09



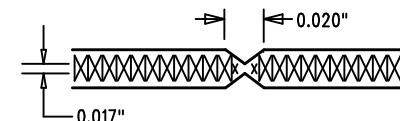
FAB Drawing

LINEAR TECHNOLOGY  
DC1409A-LTC3788EUH  
HIGH CURRENT 2-OUTPUT  
SYNCHRONOUS STEP-UP SUPPLY  
DATE: 10-05-09

SIZE	QTY	SYM	PLATED	TOL
10	73	+	YES	+/-0.003
70	2	□	NO	+/-0.003
94	17	◇	YES	+/-0.003
31	14	⊗	YES	+/-0.003
20	198	⊗	YES	+/-0.003
205	4	+ <sup>A</sup>	YES	+/-0.003
189	2	+ <sup>B</sup>	YES	+/-0.003
187	4	+ <sup>C</sup>	NO	+/-0.003

- LAYER 1 ( COMPONENT SIDE )
- LAYER 2
- LAYER 3
- LAYER 4
- LAYER 5
- LAYER 6 (SOLDER SIDE)

- FAB PER IPC-A-600.
- MATERIAL: -EPOXY FIBERGLASS, NEMA GRADE FR-4  
-FINISHED THICKNESS TO BE 0.062" +/- .005"  
-TOTAL OF 6 LAYERS WITH 2 OZ. CU ON THE OUTER LAYERS  
AND 1 OZ. CU ON THE INNER LAYERS.  
-FLAMMABILITY RATING: 94 V-0 MINIMUM.
- SIZE: CUT TO DIMENSIONS AND TOLERANCES SHOWN.  
0.00" ARE PRIMARY DATUMS.
- DRILLING: -DRILL HOLES PER SCHEDULE. PLATE THROUGH  
HOLES WITH COPPER, 0.001" THICK MIN.  
-ALL HOLE SIZES ARE SPECIFIED AFTER PLATING.  
-HOLE LOCATION TOLERANCES ARE +/-0.003"  
IN RELATION TO CENTER
- FINISH: -SMOBC USING LPI BOTH SIDES, COLOR GREEN.  
-GOLD IMMERSION BOTH SIDES.  
-FOR SILKSCREEN: BOTH SIDES USE WHITE NON-CONDUCTIVE INK.
- DO NOT ALTER ARTWORK e.g. TO ADD LOGO OR DATE CODE.  
PAD SIZE CAN BE MODIFIED TO MEET END FINISH.
- PCBS ARE TO BE RoHS COMPLIANT.
- SCORING FOR PANELIZED PCB:



.017

UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES TOLERANCES ON ANGLE ±° 0.XX" = ±0.01" 0.XXX" = ±0.005" INTERPRET DIM AND TOL PER ASME Y14.5M-1994	APPROVALS			1630 MCCARTHY BLVD MILPITAS, CA 95035 PH: (408)432-1900 LTC CONFIDENTIAL- FOR CUSTOMER USE ONLY	
	PCB DES.	INIT	DATE	TITLE: FABRICATION DRAWING HIGH CURRENT 2-OUTPUT SYNCHRONOUS STEP-UP SUPPLY	
THIRD ANGLE PROJECTION	ENG.	GORAN P.	10-05-09	SIZE	REV.
				A	3
				IC NO. LTC3788EUH DEMO CIRCUIT 1409A	
DO NOT SCALE DRAWING	SCALE: NONE		FILENAME: DC1409A-1.PCB	SHT 1 of 1	